

FIG. 1A





300

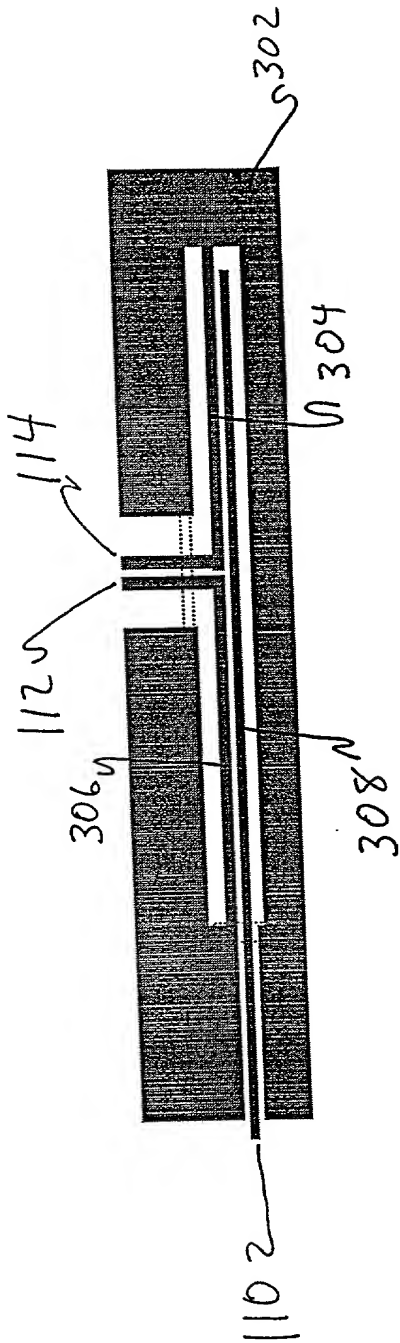
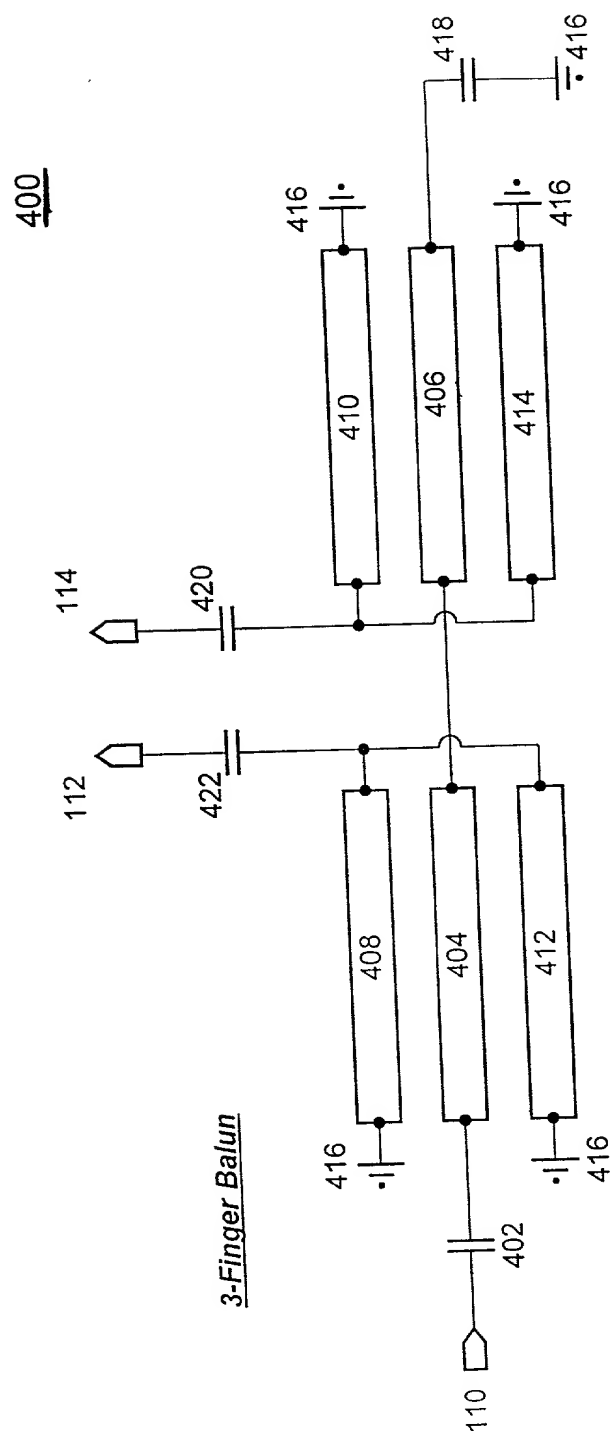


Figure 3



**FIG. 4A**

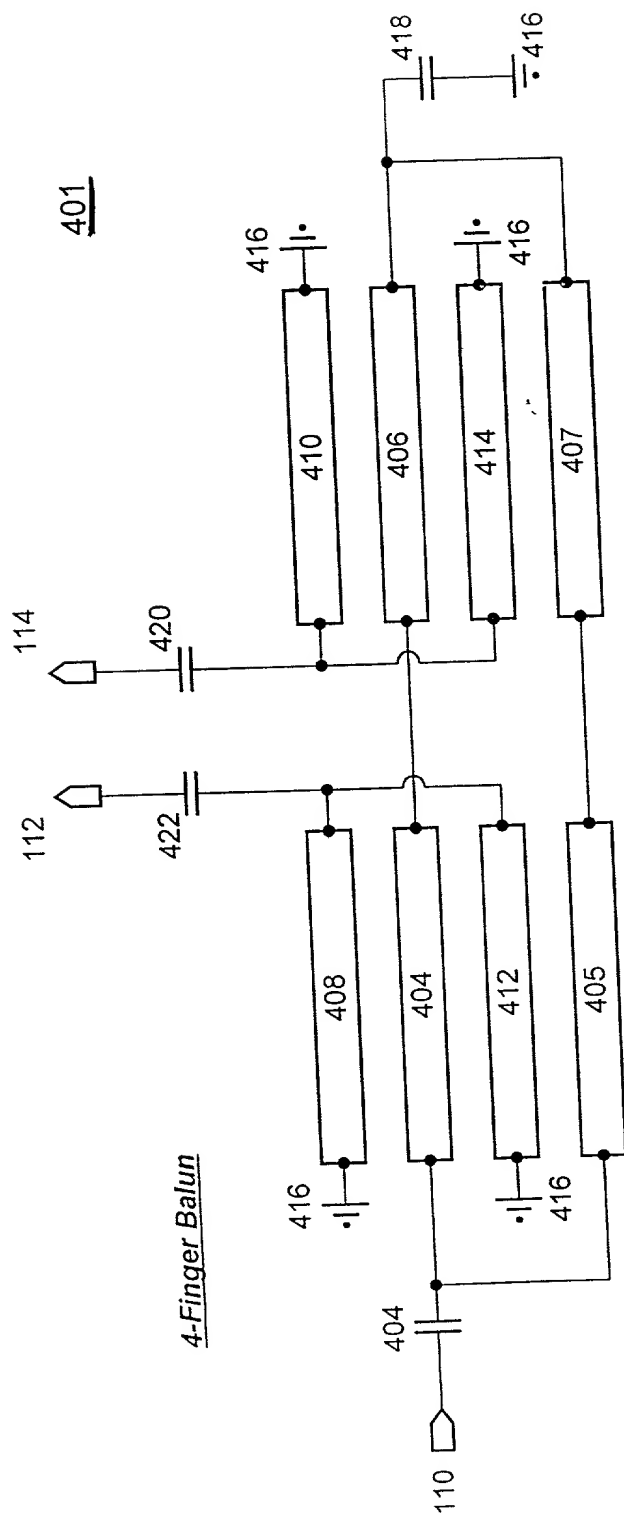


FIG. 4B



550

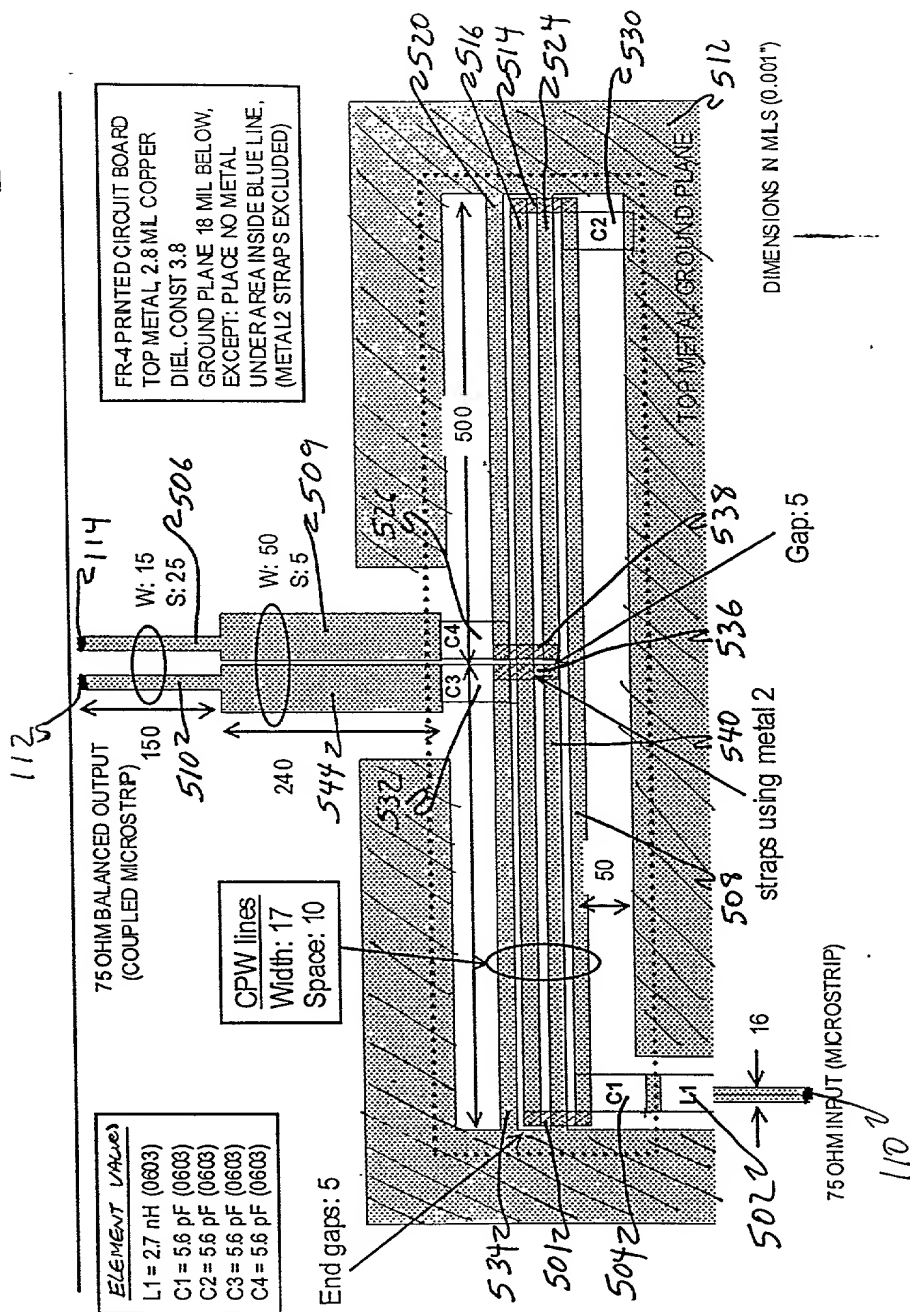


FIG 5B



# 2 Layer Printed Circuit Board

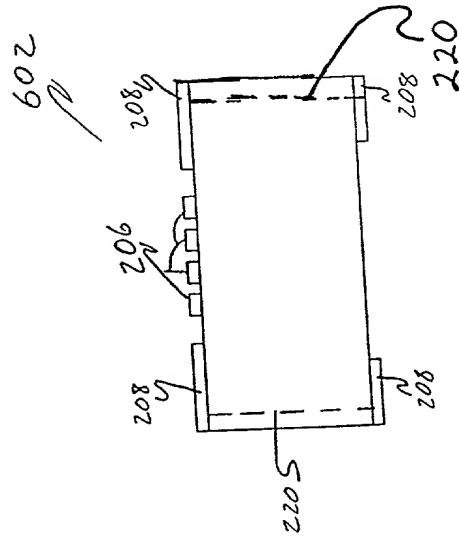


FIG 6A

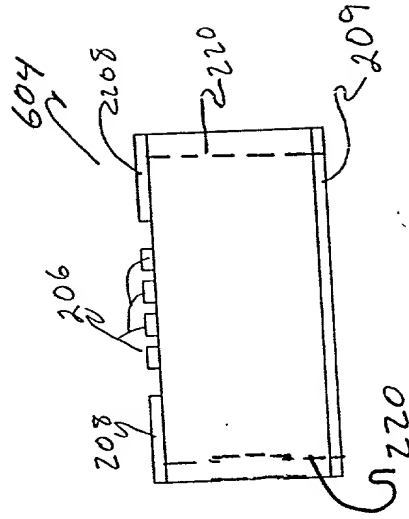


FIG 6B

# 4 Layer Printed Circuit Board

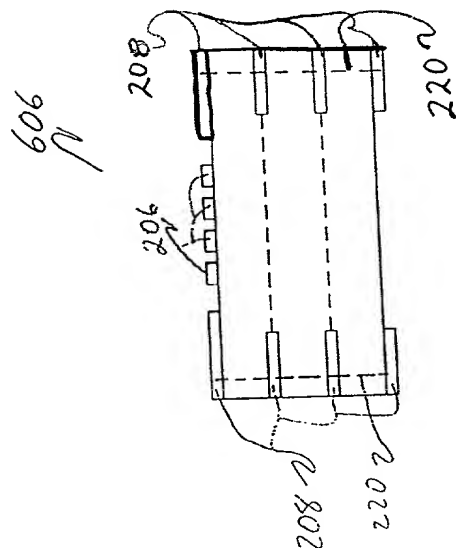


FIG 6C

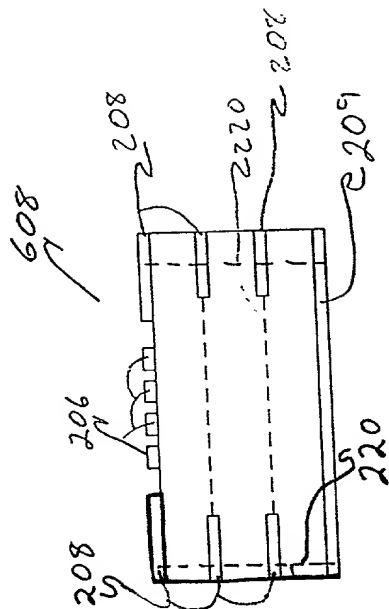


FIG 6D

6 Layer Printed Circuit Board

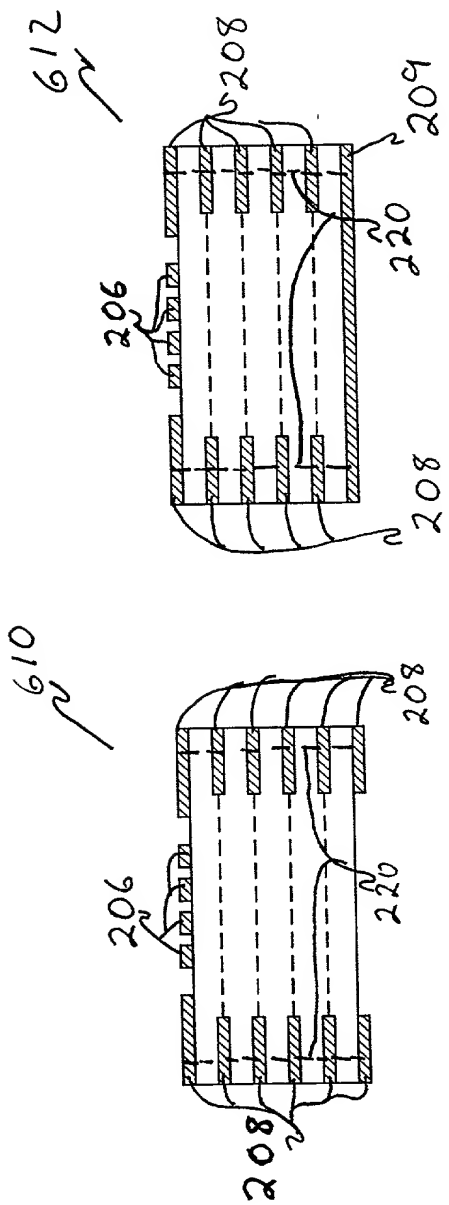


FIG. 6E

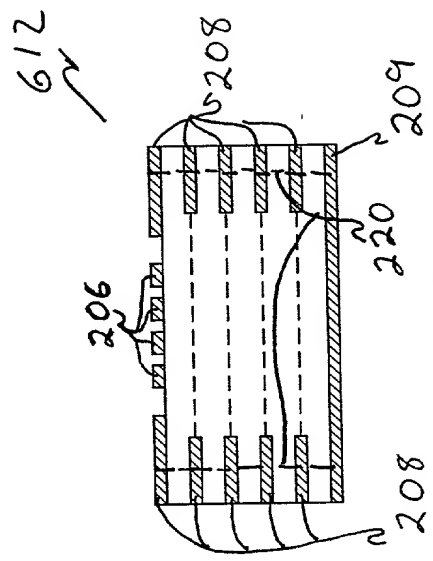


FIG. 6F

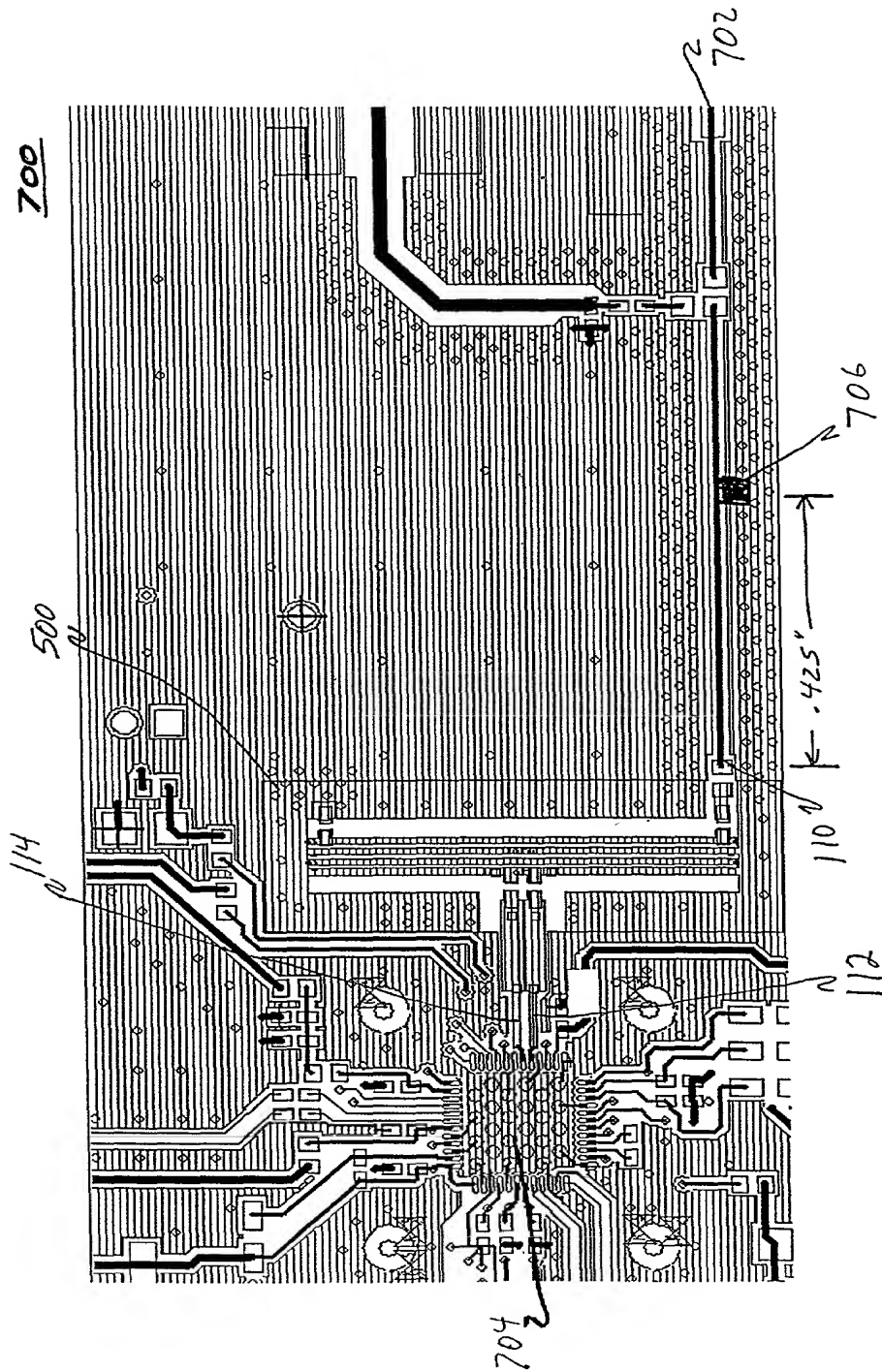


Figure 7



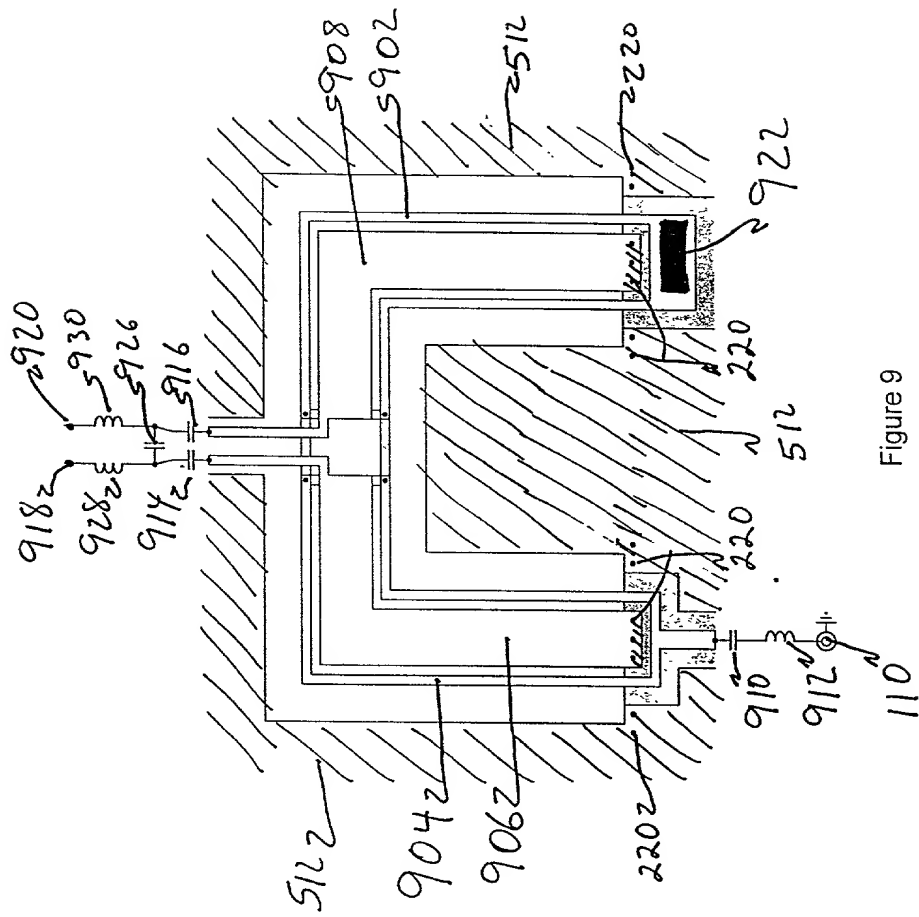
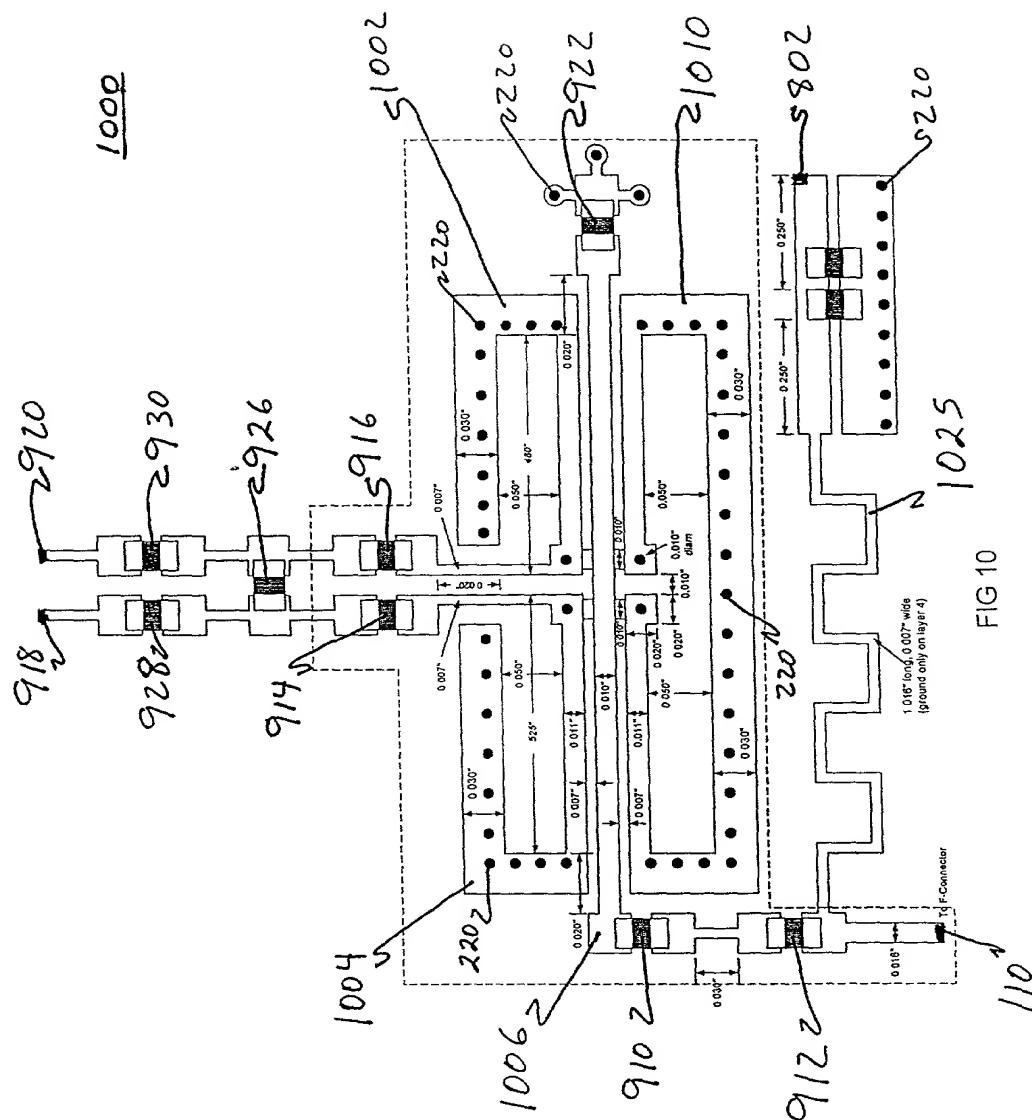
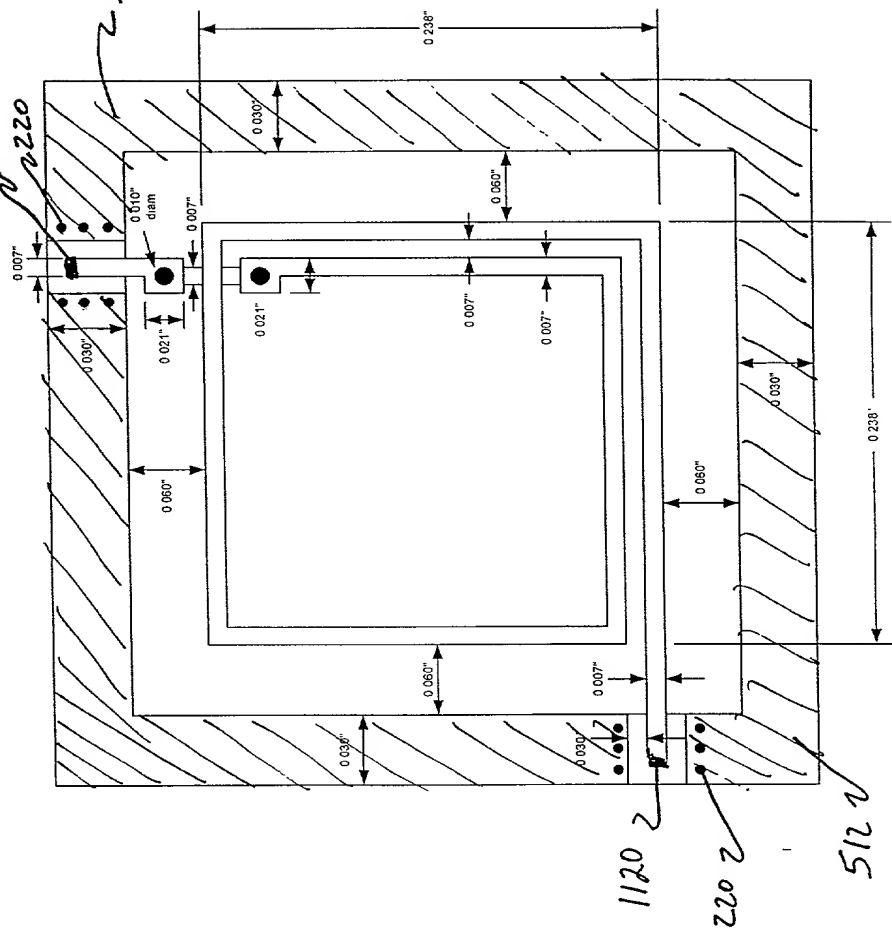


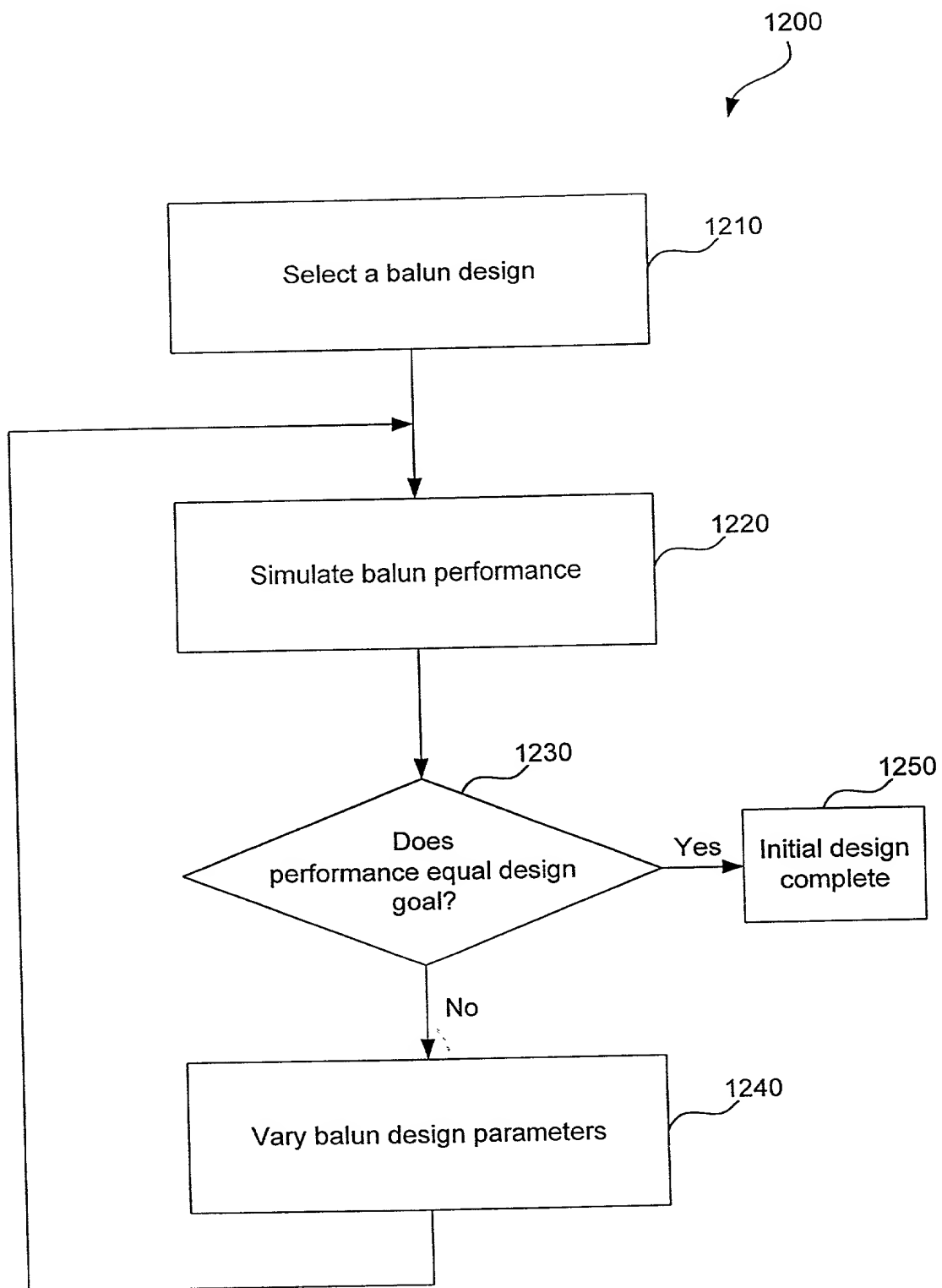
Figure 9

[illegible]

512 v







**FIG. 12**

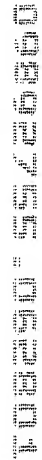
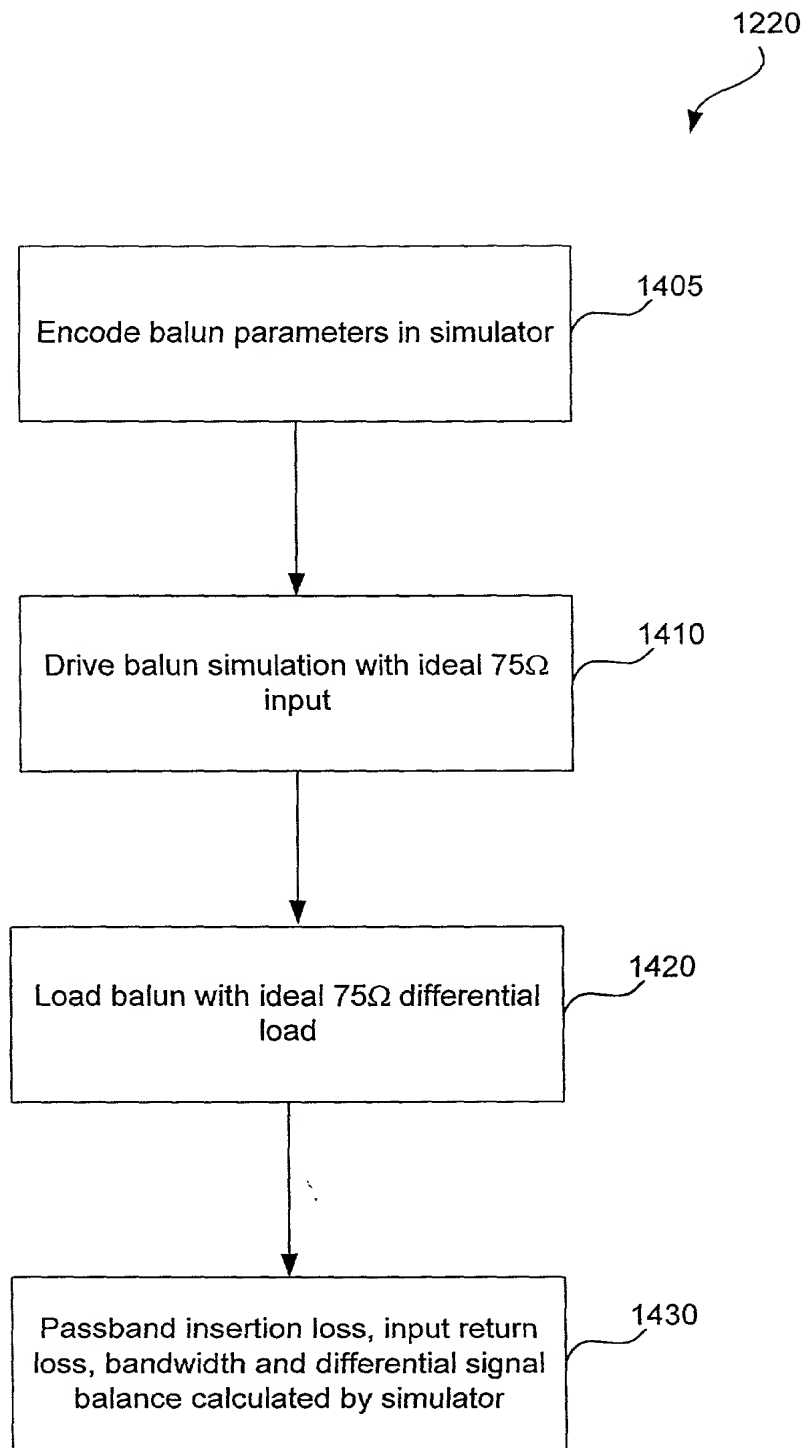


FIG. 13



**FIG. 14**



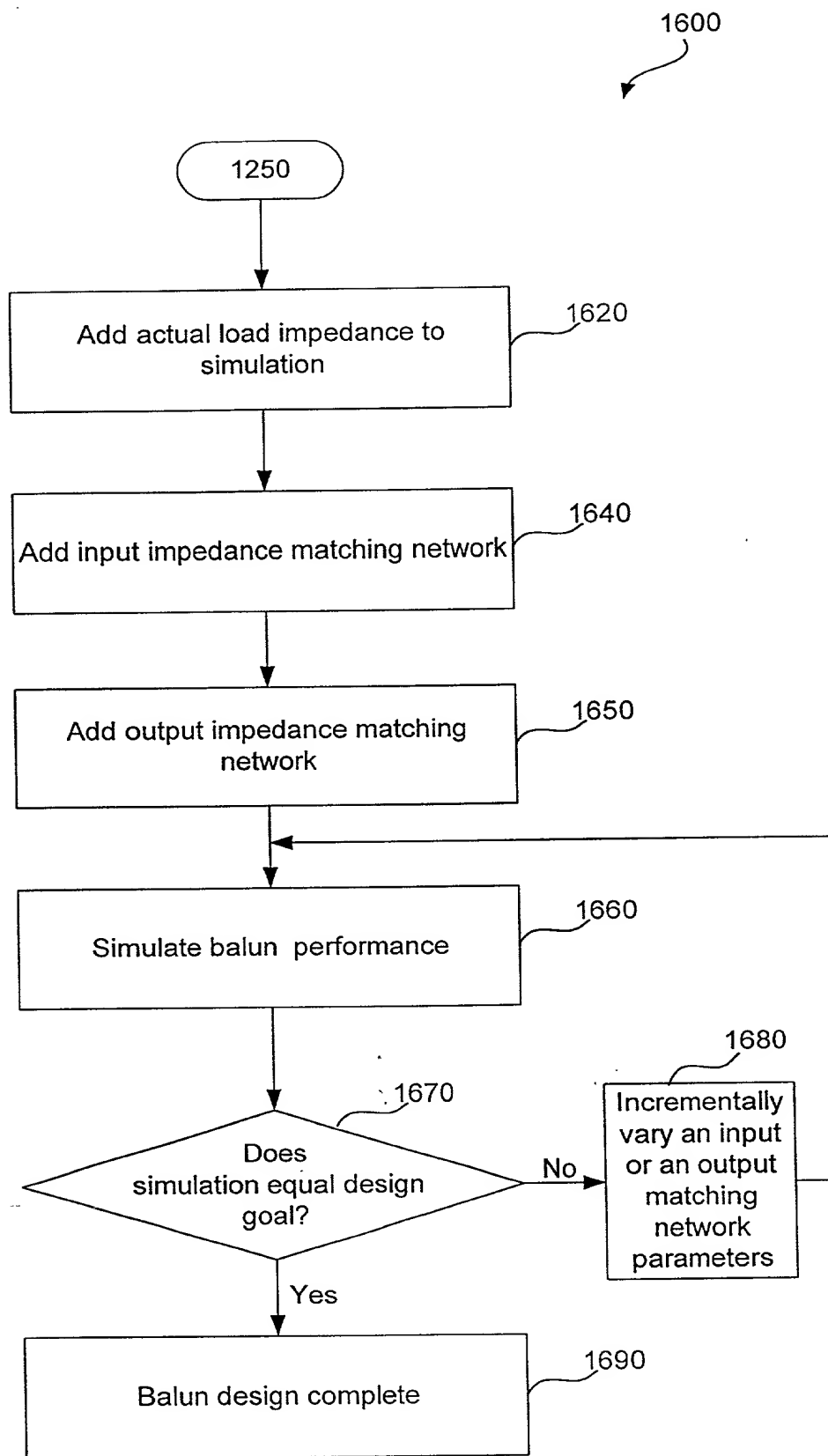


FIG. 16